



2818

AMENDMENT TRANSMITTAL LETTER

Docket No.
M4065.0478/P478Application No.
09/988,627Filing Date
November 20, 2001Examiner
Not Yet AssignedArt Unit
2818

Applicant(s): Glen Hush, et al

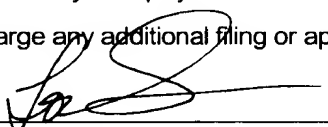
Invention: COMPLEMENTARY BIT PCRAM SENSE AMPLIFIER AND METHOD OF OPERATION

TO THE COMMISSIONER FOR PATENTS

Transmitted herewith is an amendment in the above-identified application.

The fee has been calculated and is transmitted as shown below.

CLAIMS AS AMENDED					
	Claims Remaining After Amendment	Highest Number Previously Paid	Number Extra Claims Present	Rate	
Total Claims	46	- 58 =		x	0.00
Independent Claims	5	- 5 =		x	0.00
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					
Other fee (please specify):					
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT:					0.00

☒ Large Entity☐ Small Entity☒ No additional fee is required for this amendment.☐ Please charge Deposit Account No. _____ in the amount of \$ _____.
A duplicate copy of this sheet is enclosed.☐ A check in the amount of \$ _____ to cover the filing fee is enclosed.☐ Payment by credit card. Form PTO-2038 is attached.☒ The Commissioner is hereby authorized to charge and credit Deposit Account No. 04-1073
as described below. A duplicate copy of this sheet is enclosed.☒ Credit any overpayment.☒ Charge any additional filing or application processing fees required under 37 CFR 1.16 and 1.17.
Thomas J. D'Amico
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Dated: February 12, 2003

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Docket No.: M4065.0478/P478
(PATENT)

#71A
Hush
4/30/03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Glen Hush, et al.

Application No.: 09/988,627

Group Art Unit: 2824

Filed: November 20, 2001

Examiner: N. Nguyen

For: COMPLIMENTARY BIT PCRAM SENSE
AMPLIFIER AND METHOD OF
OPERATION

AMENDMENT

Box Non-Fee Amendment

Commissioner for Patents
Washington, DC 20231

Dear Sir:

In response to the Office Action dated December 17, 2002 (Paper No. 5),
please amend the claims as follows.

In the claims:

Please replace claims 42 and 53 with the following.

42. (Amended) A method of operating a programmable conductor memory device
comprising:

storing a binary value as respective complementary resistance states in a first and
second programmable conductor memory element;

determining a binary value stored in one of said memory elements by discharging
respective voltages through said memory elements and comparing the discharging voltages.